



- ☒ L9: (278664) (read-out) (readout)
- ☒ L10: (1385940) (interchanging)
- ☒ L11: (259) 3 and 5 and 7
- ☒ L13: (6202) 9 and 10 and 3
- ☒ L14: (33) 3 same 5 same 7
- ☒ L15: (865) 9 same 10 same 3

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DBs US-PGPUB; USPAT; EPO; JPO

Default operator: OR

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|----|------|-----|---------|---|---------------------------|
| 1 | BRS | L1 | 1379 | read-out adj circuit | US-PGPUB; USPAT; EPO; JPO |
| 2 | BRS | L2 | 36 | interchanging adj circuit | US-PGPUB; USPAT; EPO; JPO |
| 3 | BRS | L3 | 34705 | sense adj amplifier | US-PGPUB; USPAT; EPO; JPO |
| 4 | BRS | L4 | 1 | 1 and 2 and 3 | US-PGPUB; USPAT; EPO; JPO |
| 5 | BRS | L5 | 4863 | (read-out adj circuit) (readout adj circuit) (read adj out adj circuit) | US-PGPUB; USPAT; EPO; JPO |
| 6 | BRS | L7 | 1199709 | (interchanging adj circuit) switch mux | US-PGPUB; USPAT; EPO; JPO |
| 7 | BRS | L9 | 278664 | (read-out) (readout) (read adj out) | US-PGPUB; USPAT; EPO; JPO |
| 8 | BRS | L10 | 1385940 | (interchanging) switch mux isolate | US-PGPUB; USPAT; EPO; JPO |
| 9 | BRS | L11 | 259 | 3 and 5 and 7 | US-PGPUB; USPAT; EPO; JPO |
| 10 | BRS | L13 | 6202 | 9 and 10 and 3 | US-PGPUB; USPAT; EPO; JPO |
| 11 | BRS | L14 | 33 | 3 same 5 same 7 | US-PGPUB; USPAT; EPO; JPO |
| 12 | BRS | L15 | 865 | 9 same 10 same 3 | US-PGPUB; USPAT; EPO; JPO |

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- L1: (1379) read-out adj circ
- L2: (36) interchanging adj c
- L3: (34705) sense adj ampli
- L4: (1) 1 and 2 and 3
- L5: (4863) (read-out adj cir
- L7: (1199709) (interchangin
- L8: (1) 4 and 5 and 7

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 DBs US-PGPUB; USPAT; EPO; JPO
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| 7 | BRS | L8 | 1 | 4 and 5 and 7 | US-PGPUB; USPAT; EPO; JPO |

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